

Application Data Sheet

Application Information

Application type::	Regular
Subject matter::	Utility Patent
CD-ROM or CD-R::	None
Number of CD disks::	0
Number of copies of CDs::	0
Sequence submission?::	No
Computer readable form (CRF)?::	No
Number of copies of CRF::	0
Title::	PWM Buffer Circuit for Adjusting a Frequency and a Duty Cycle of a PWM Signal
Attorney docket number::	CHI103030/em
Request for early publication?::	No
Request for non-publication?::	No
Suggested drawing figure::	FIG. 3
Total drawing sheets::	4
Small entity?::	No

Applicant Information

Applicant authority type::	Inventor
Primary citizenship country::	Taiwan, R.O.C.
Status:	Full capacity
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Middle name::	N/a
Family name::	CHI10
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Country of mailing address::	Taiwan, R.O.C.
Postal or zip code of mailing address::	N/a
Applicant authority type::	Inventor
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Status:	Full capacity
Given name::	Wen-shi
Middle name::	N/a
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City of mailing address::	Taoyuan County
State or province of mailing address::	N/a
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Postal or zip code of mailing address::	N/a

Correspondence Information

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Representative Information

Representative customer number:: 23364

Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
None			

Foreign Priority Information

Country::	Application number::	Filing Date::	Priority claimed::
Taiwan, R.O.C.	92115929	2003/6/12	Yes.

Assignee Information

Assignee name:: DELTA ELECTRONICS, INC.
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